

# 2 Phe A  
SUC 2.22-01

**PATENT APPLICATION**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of:

NOMURA et al.

Application No.: New Appln.

Filed: October 19, 2000

Attorney Dkt. No.: P108066-00014

For: SELF-TEST CIRCUIT AND MEMORY DEVICE INCORPATING IT

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
Washington, D.C. 20231

Date: October 19, 2000

Sir:

Prior to initial examination of the application, please amend the above-identified application as follows:

**IN THE SPECIFICATION:**

Please amend the specification as follows:

Page 10, line 23, delete "Figure 15 is an operation timing chart of a test" and substitute therefore, --/Figure 15A-15C is an operation timing chart of a test --

a 1

Page 11, line 11, delete "Figure 21 is a view showing an example of an output" and substitute therefore, --/Figure 21A and 21B is a view showing an example of an output --

a 2

Page 12, line 3, delete "Figure 29 is a view illustrating the reset function" and substitute therefore, --/Figure 29A-29C is a view illustrating the reset function --

a 3

0069115-104900